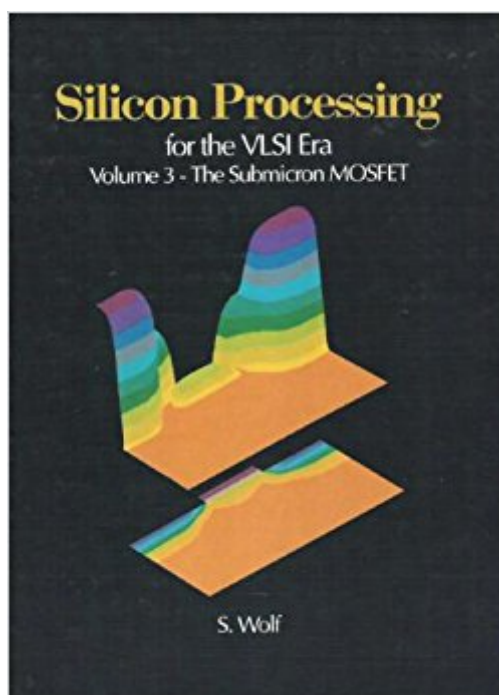


The book was found

# Silicon Processing For The VLSI Era, Vol. 3: The Submicron MOSFET



## Synopsis

Volume 3: The Submicron MOSFET. Treats the topics of submicron MOSFET device physics and the relationship between such device physics and submicron MOSFET fabrication. DLC: Integrated circuits - Very large scale.

## Book Information

Hardcover: 722 pages

Publisher: Lattice Press (January 1994)

Language: English

ISBN-10: 0961672153

ISBN-13: 978-0961672157

Product Dimensions: 1.5 x 7.5 x 9.8 inches

Shipping Weight: 3 pounds (View shipping rates and policies)

Average Customer Review: 5.0 out of 5 stars 2 customer reviews

Best Sellers Rank: #1,390,784 in Books (See Top 100 in Books) #57 in Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > VLSI & ULSI #310 in Books > Textbooks > Engineering > Electrical & Electronic Engineering #2395 in Books > Textbooks > Medicine & Health Sciences > Medicine > General

## Customer Reviews

This book arrived on time. Very fast service and received in good condition. This book is an excellent reference for semiconductor device researcher and professionals. If you have a good knowledge of device theory and want to learn more about the device modeling and then it is an excellent book for you. This book is not meant for BS EE rather for MS/PHD guys who are practicing semiconductor device modeling. It is my personal feeling.

Though this was not my first book on devices, I think it is an excellent resource for beginners.

[Download to continue reading...](#)

Silicon Processing for the VLSI Era, Vol. 3: The Submicron MOSFET Silicon Processing for the VLSI Era, Vol. 4: Deep-Submicron Process Technology Silicon Processing for the VLSI Era, Vol. 1: Process Technology Silicon Processing for the VLSI Era, Vol. 2: Process Integration Chip Design for Submicron VLSI: CMOS Layout and Simulation Silicon Wafer Bonding Technology for VLSI and MEMS Applications (Emis Processing Series, 1) Silicon VLSI Technology: Fundamentals, Practice,

and Modeling Silicon VLSI Technology VLSI Fabrication Principles: Silicon and Gallium Arsenide, 2nd Edition VLSI DESIGN SIMPLE AND LUCID EXPLANATION: vlsi design for students VLSI Test Principles and Architectures: Design for Testability (The Morgan Kaufmann Series in Systems on Silicon) CMOS VLSI Engineering: Silicon-on-Insulator (SOI) Circuits, Interconnections, and Packaging for Vlsi (Addison-Wesley VLSI systems series) MOSFET Models for SPICE Simulation: Including BSIM3v3 and BSIM4 VLSI Digital Signal Processing Systems: Design and Implementation Vlsi Analog Signal Processing Circuits Let's Grill! Best BBQ Recipes Box Set: Best BBQ Recipes from Texas (vol.1), Carolinas (Vol. 2), Missouri (Vol. 3), Tennessee (Vol. 4), Alabama (Vol. 5), Hawaii (Vol. 6) American History by Era - The Colonial Period: 1607-1750 Vol. 2 (paperback edition) (American History by Era) Discrete-Time Signal Processing (3rd Edition) (Prentice-Hall Signal Processing Series) Materials Processing: A Unified Approach to Processing of Metals, Ceramics and Polymers

[Contact Us](#)

[DMCA](#)

[Privacy](#)

[FAQ & Help](#)